

FIG. 1

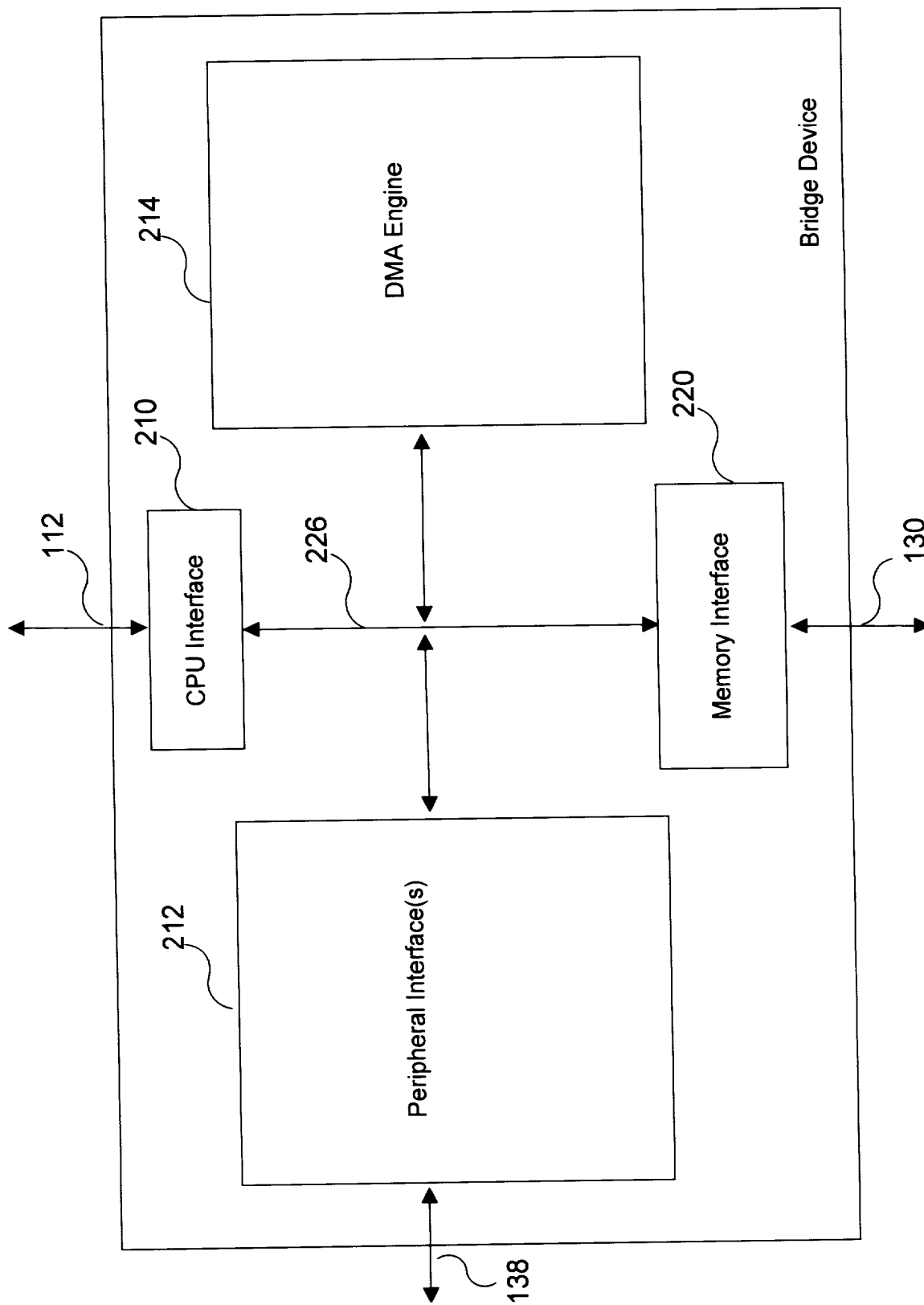


FIG. 2

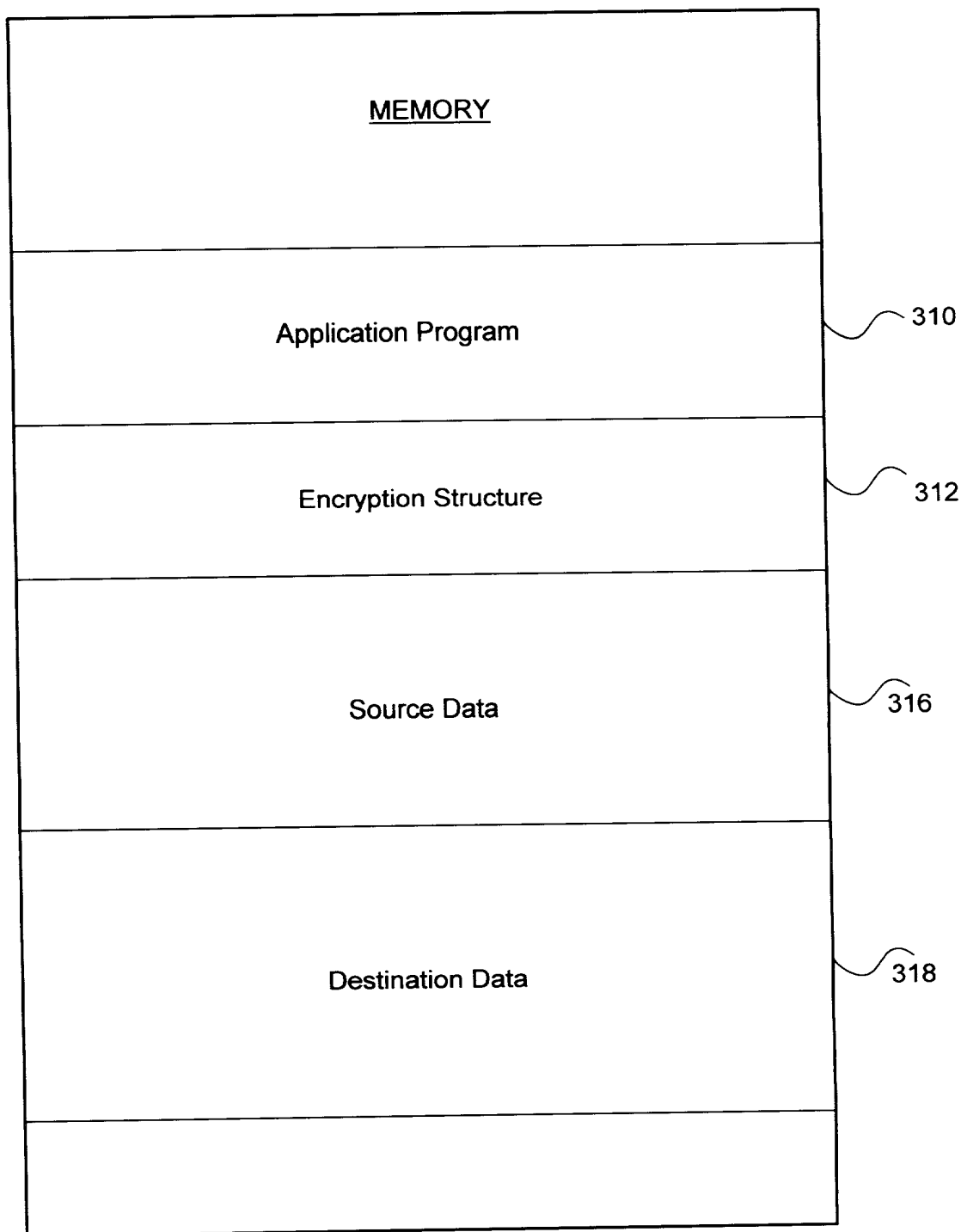
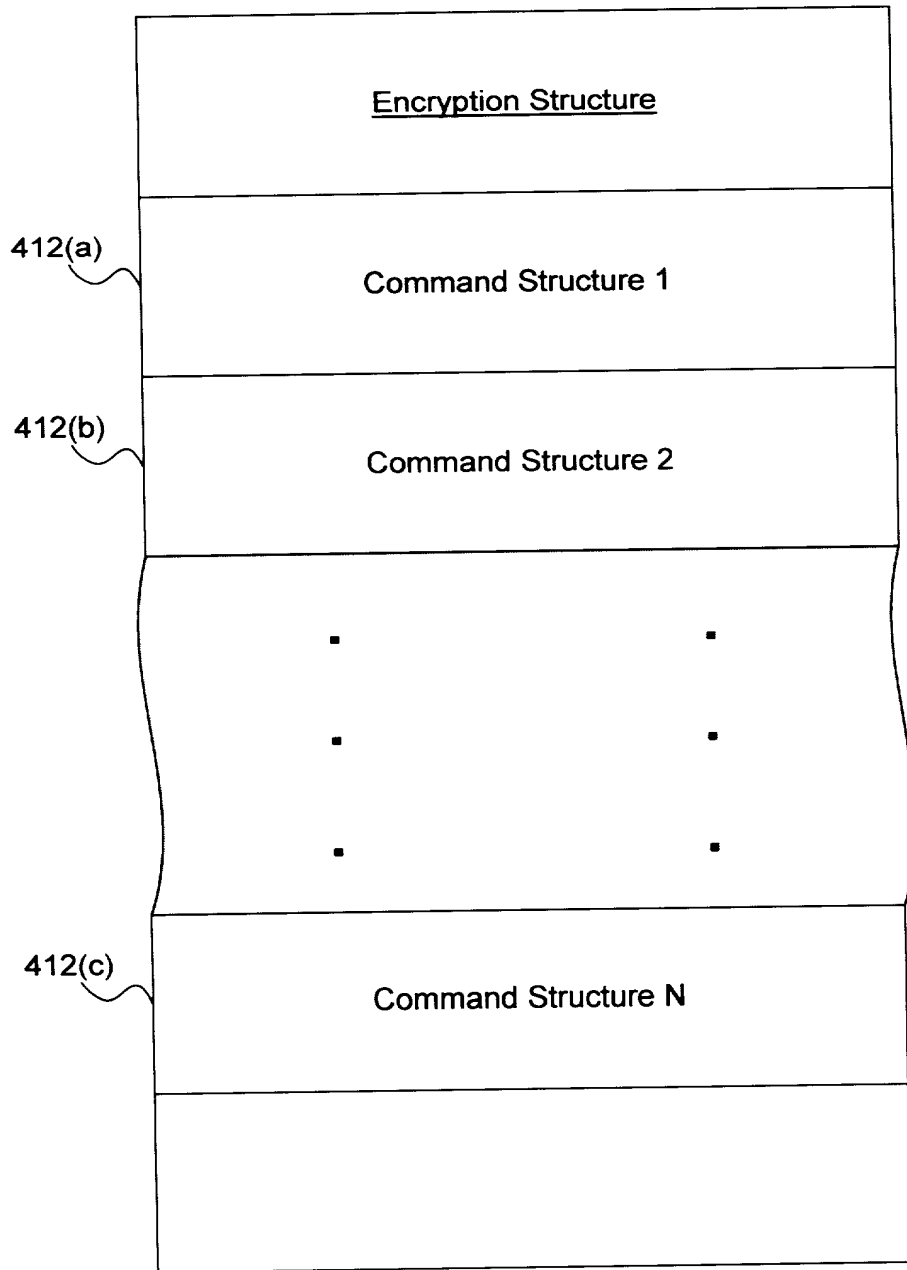


Fig. 3



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FIG. 4

Command Structure

```
typedef struct DMA_Cmd
{
    unsigned controlStatus; 538
    unsigned int sourceAddr;
    unsigned int destAddr;
    unsigned int byteCount;
    DMA_Cmd *nextDMACmd;
} DMA_Command;

512
516
522
526
// Starting source address
// Starting destination address
// Number of bytes to transfer
// Ptr to next command struct
```

FIG. 5

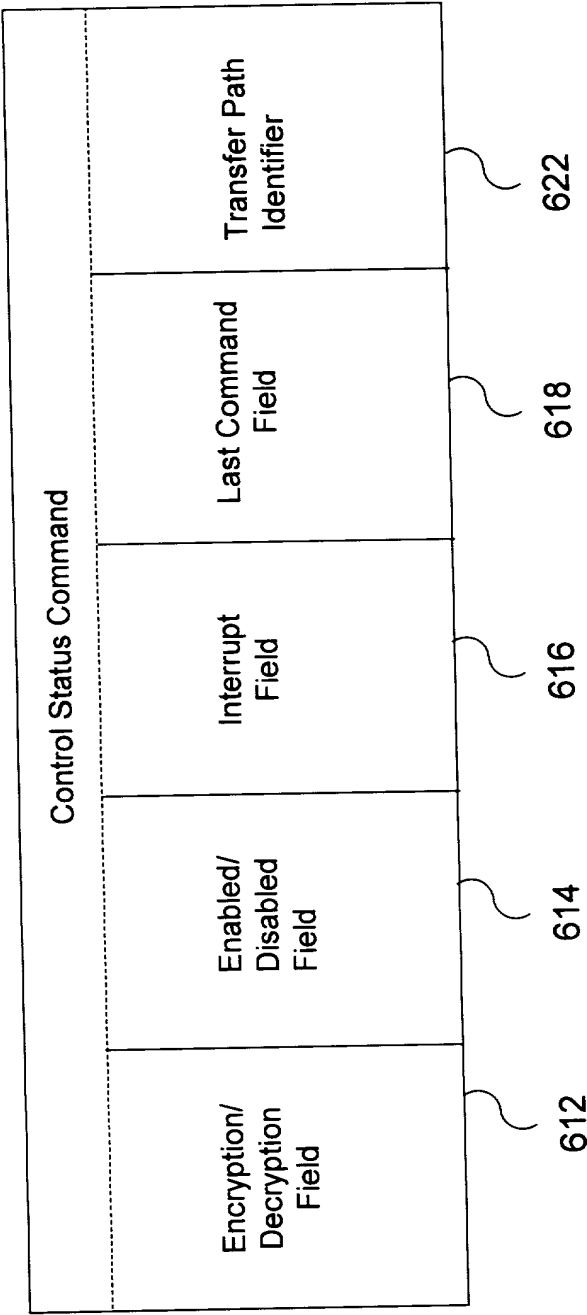


FIG. 6

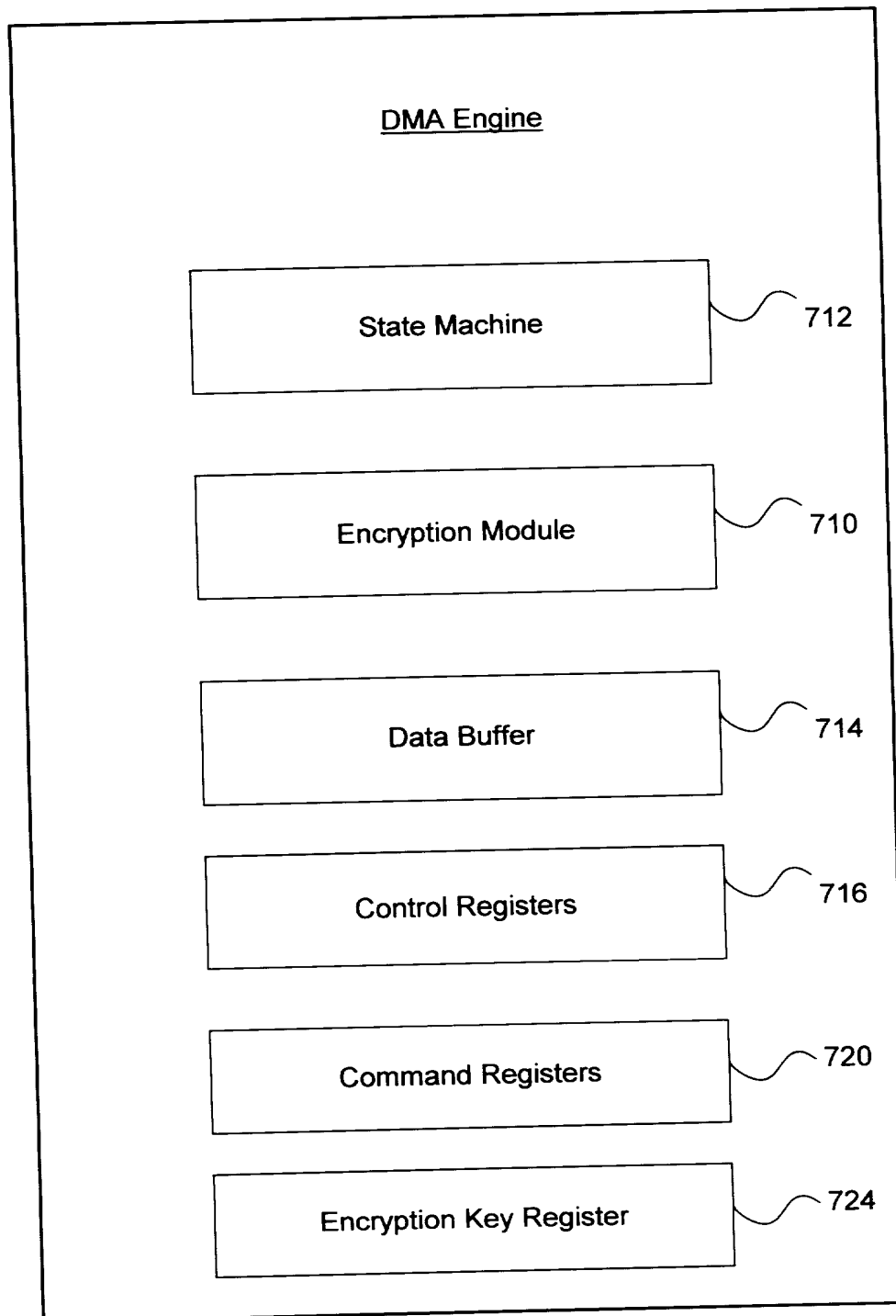


Fig. 7

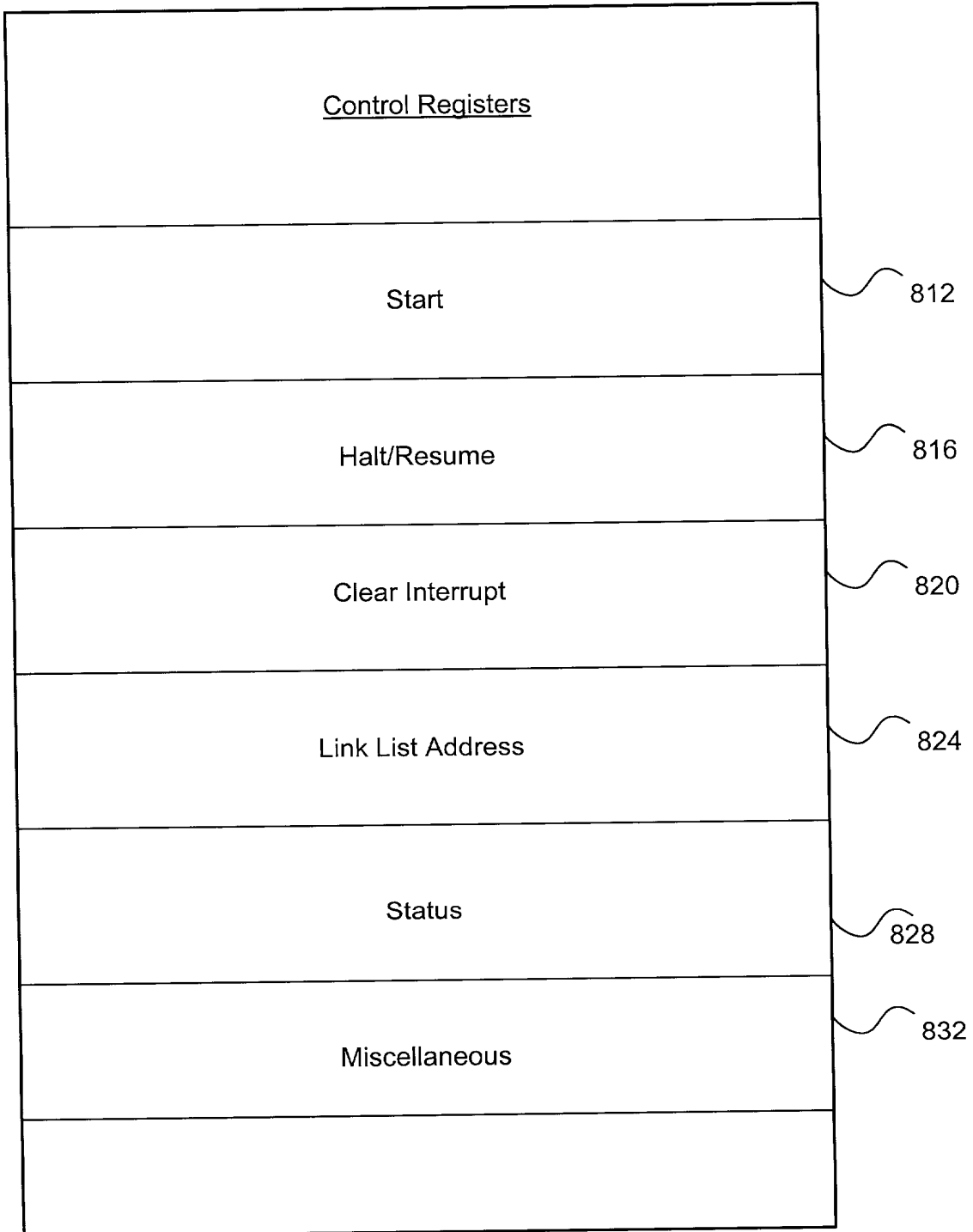


Fig. 8



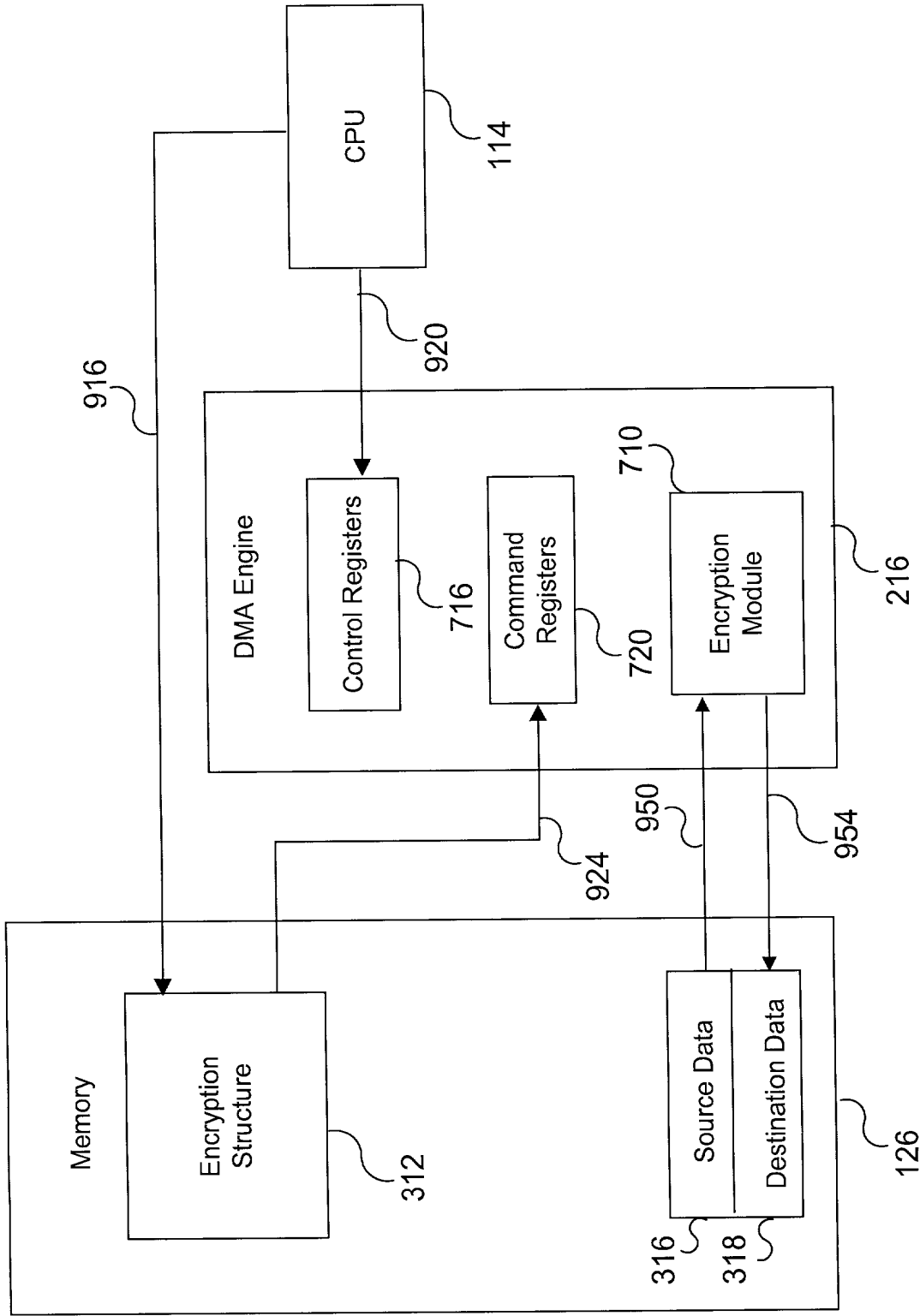


FIG. 9

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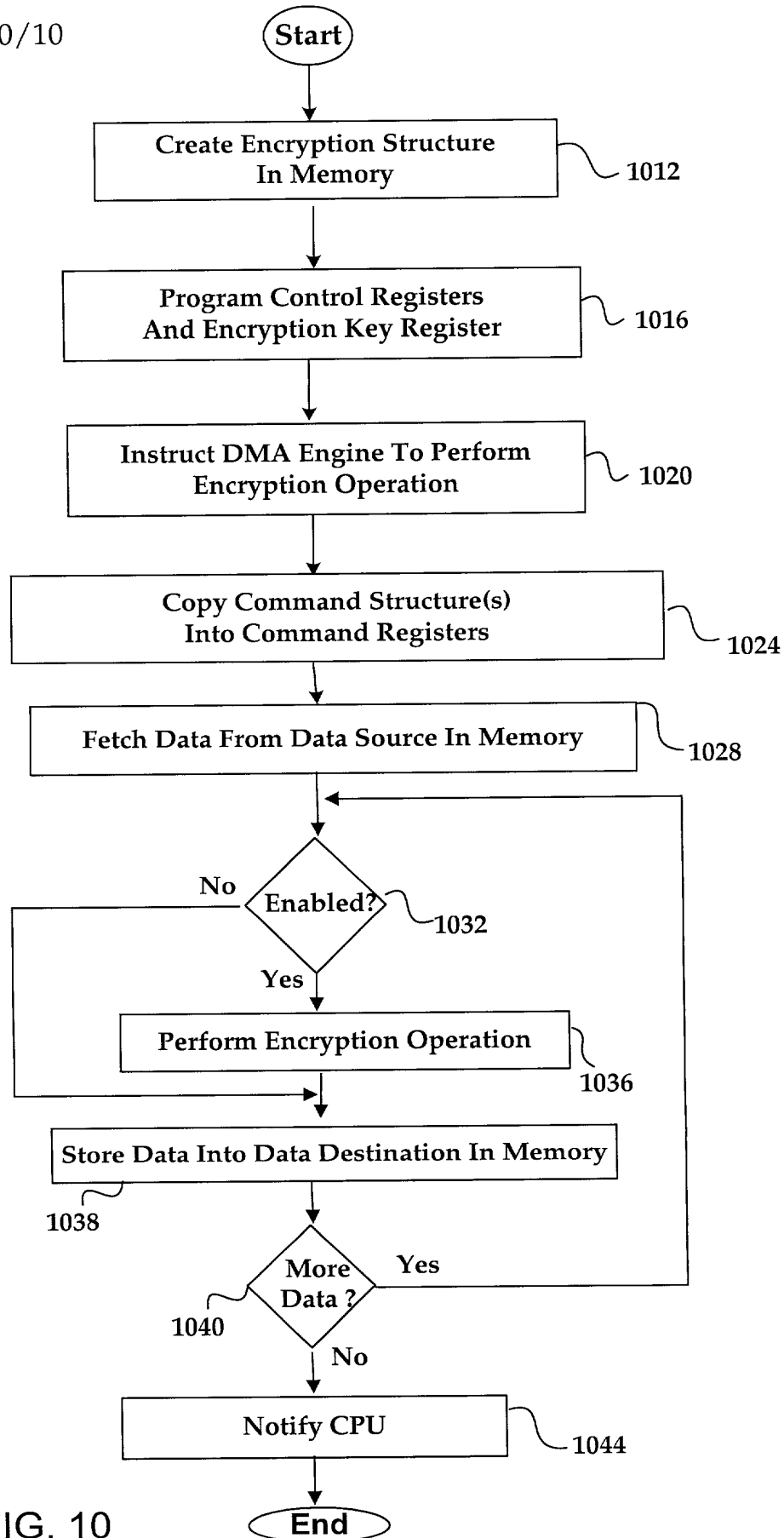


FIG. 10